

MPS 6520 VERSUS MC6820/MC6821

The MPS 6520 Peripheral Adapter is a 16 line (2 port) bi-directional I/O device with four peripheral control/interrupt lines. The MPS 6520 is pin and functionally compatible with the MC6820 Peripheral Interface Adapter from Motorola/Hitachi. The MPS 6520 is pin compatible with the MC6821 PIA, but the functional differences are outlined in the table below.

<u>CHARACTERISTIC</u>	<u>MPS 6520 MAX</u>	<u>MC6821 MAX</u>
I_{IL} , Input Low Current PA0-PA7, CA2 ($V_{IL} = 0.4Vdc$)	-1.6mAdc	-2.4mAdc
I_{LOAD} ($V_{CC} = min., V_{OL} = 0.4Vdc max.$)	1.6mAdc	3.2mAdc

The other functional difference between the MPS 6520 and the MC6821 is that the 6520 has a max. enable cycle time of 25 μ sec. while the MC6821 is fully static. If the above differences are not critical to the user, than the MPS 6520 may be substituted for the MC6821 as well as the MC6820.